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INFORMATION DISCLOSURE		Filing Date	November 28, 2000	
STATE	EMENT BY APPLICANT	First Named Inventor	Stephen M. Trimberger	26 40 80
		Group Art Unit	Not Yet Known	52

Examiner Name

Attorney Docket Number

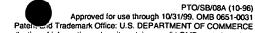
U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No.¹	U.S. Patent Document Kind Cod Number (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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INFORMATION DISCLOSURE				Application Number	Not Yet Known	
				Filing Date	November 28, 2000	
STATEMENT BY APPLICANT			APPLICANT	First Named Inventor	Stephen M. Trimberger	
				Group Art Unit	Not Yet Known	
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		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
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		Xilinx Application Note, "Configuration Issues: Power-Up, Volatility, Security, Battery Back-Up", XAPP 092, November 24, 1997 (Version 1.1).	
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		Bruce Schneier "Applied Cryptography: Protocols, Algorithms, and Source Code in C", Second Edition, Copyright 1996, published by John Wiley & Sons, Inc.; Chapter 9, pages 193-194 and 200-203; Chapter 12, pages 265-301 and Chapter 15, pages 360-361	
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		Xilinx, Application Note, XAPP138, "Virtex FPGA Series Configuration and Readback", published October 4, 2000, available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124.	
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